

AMENDMENTS TO THE SPECIFICATION

*Please replace the paragraph at page 7, lines 4-10, with the following amended paragraph:*

Refer now to Figure 3, which is an example of a main memory representation of a different super instruction. The label ADDR\_START indicates the starting address of the super instruction. The first two words are tag words containing a list of tuples ((F1, 4), (F2, 1), (F3, 1), (F5, 2)). The asterisk indicates an end marker that terminates the list of tuples. The final marking N indicates the total number of cycles executed by the super instruction. Each of the symbols described in this discussion must be assigned a detailed binary code. The actual binary codes used are a matter of design choice and will not be discussed in detail here.